

ABSTRACT OF THE DISCLOSURE

In an apparatus according to this invention, a single synchronous photography timing controller supplies cameras for synchronous photography, with an external clock signal
5 for controlling progress of a photographic sequence, an external reset signal for resetting the photographic sequence, and an external trigger signal for starting high-speed photography, all in a coordinated manner. Consequently, a timewise corresponding relationship is maintained between the external
10 clock signals and between the external reset signals being supplied. The external reset signals reset photographic sequences of the respective cameras, so that the photographic sequences also progress in a timewise corresponding relationship. The high-speed photography of
15 the cameras started by the external trigger signals supplied in the coordinated manner progress in a strictly timewise corresponding relationship. Thus, the cameras are synchronized accurately to perform the high-speed photography.